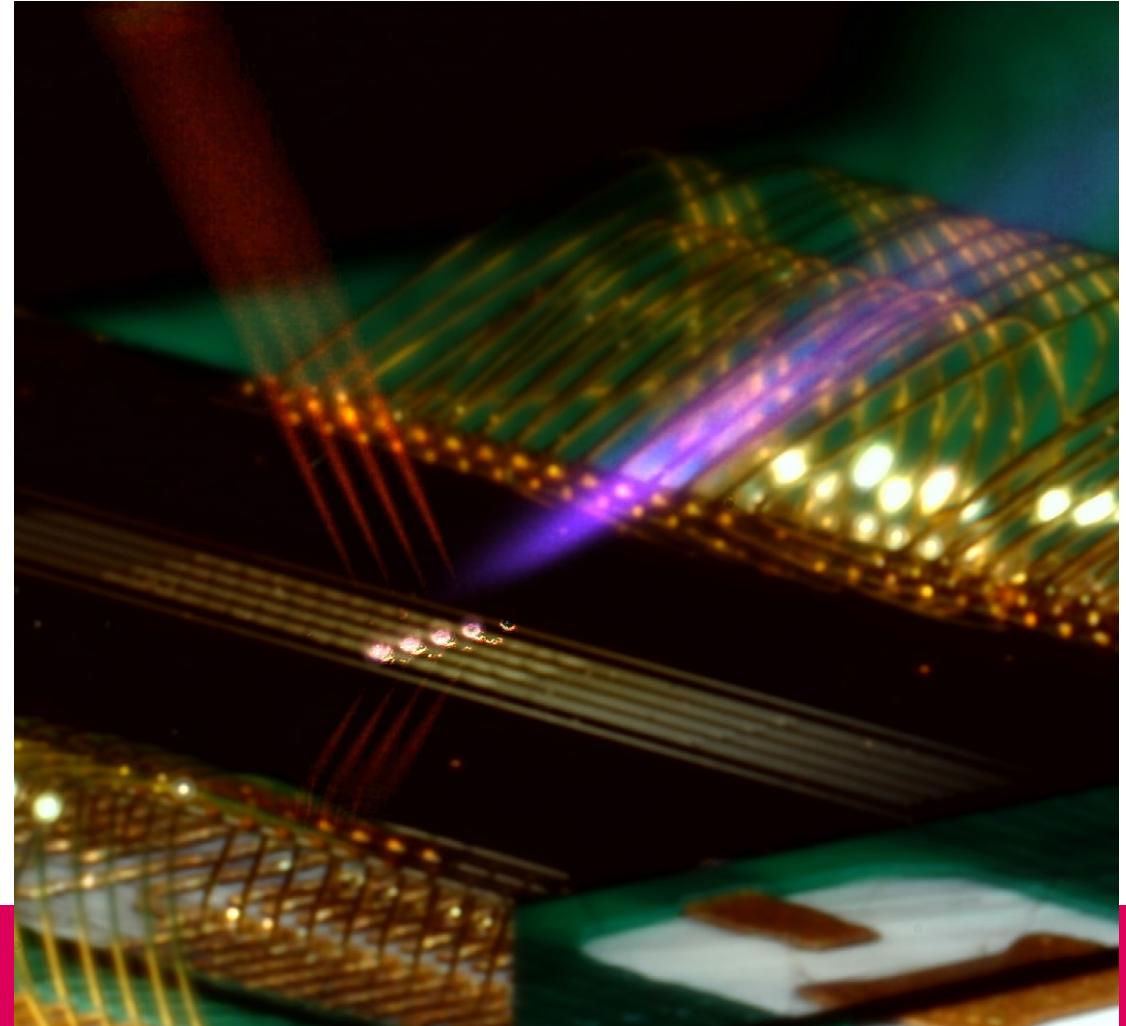


PSI

Integrated photonics as enabler of scalable quantum technology

Cyber Alp Retreat 2025



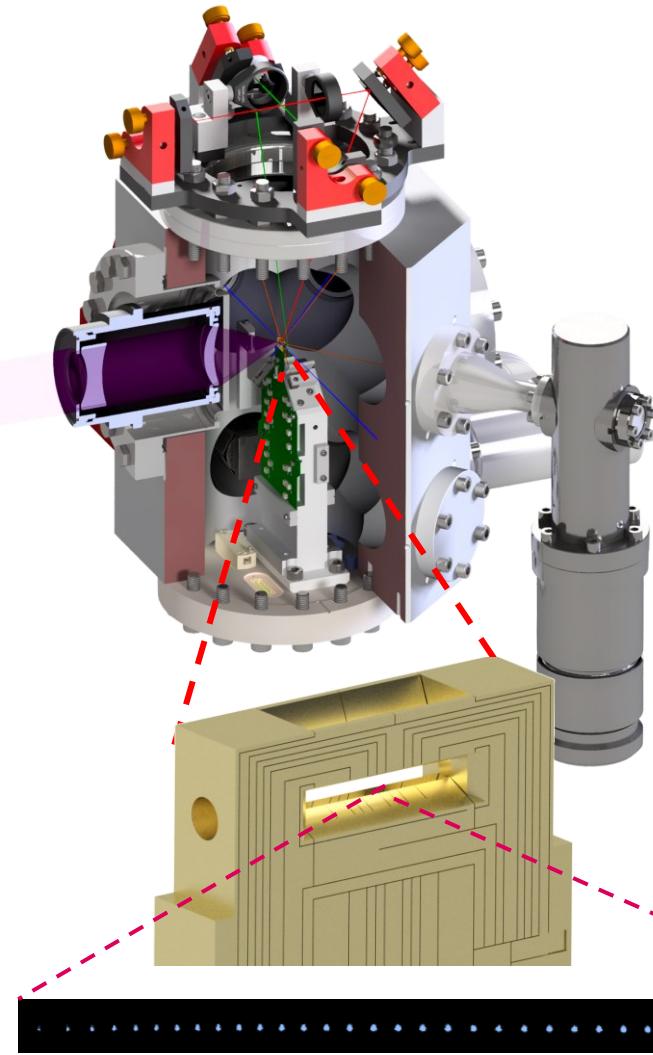
Sofia Cano Castro
Davos, 19.06.2025

Photo: Noah Tajwar / Tereza Viskova

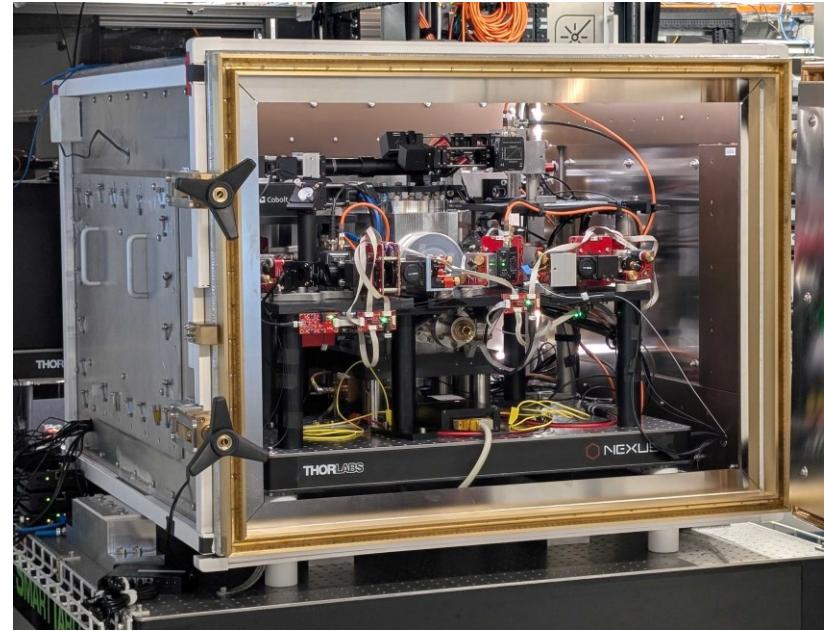


Quantum Computing Hub

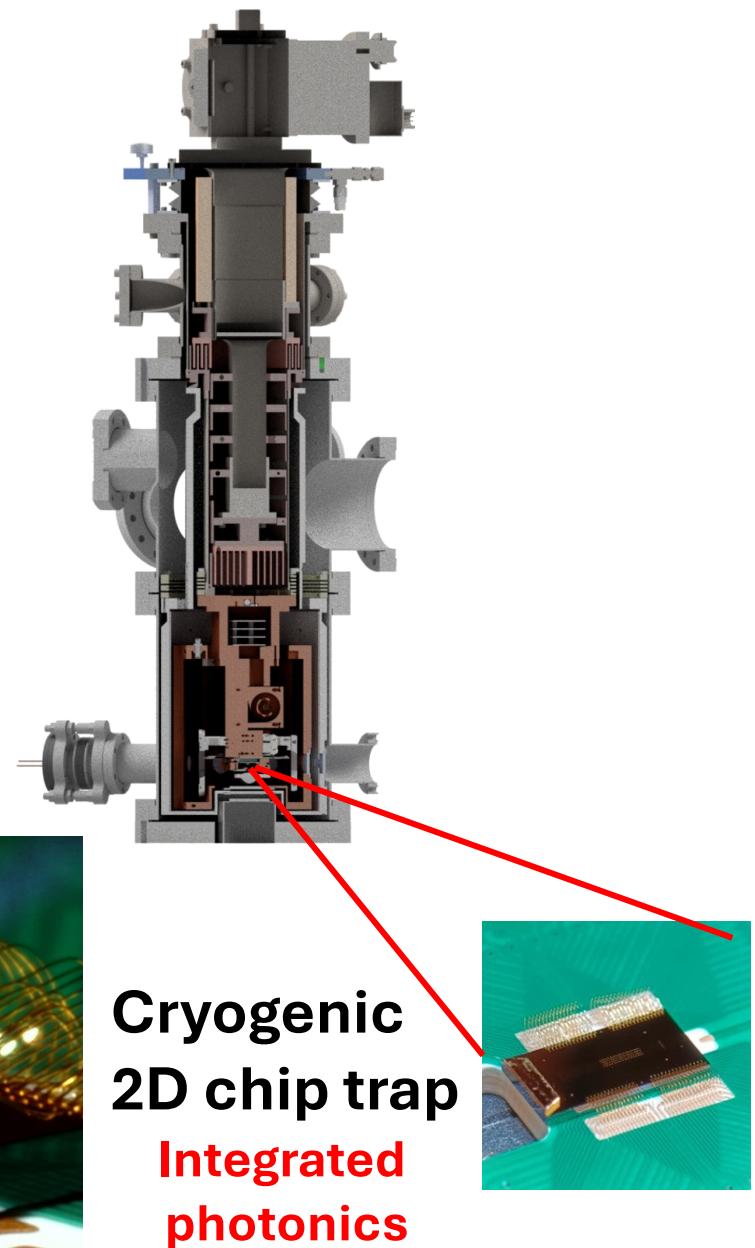
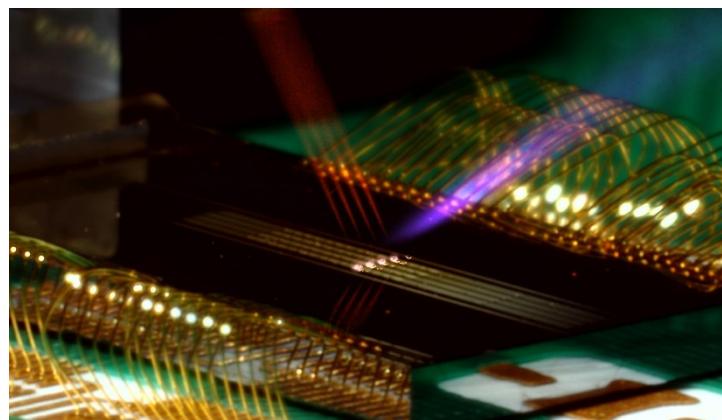
Trapped ion quantum computing at the Quantum Computing Hub



33 Ca^+ (supports up to 50 physical qubits)
Autonomous runs + remote operation



Room temperature
3D trap



Cryogenic
2D chip trap
Integrated
photonics

Outline



- I. Introduction
- II. Photonic integration
 - a) Motivation
 - b) Status
- III. Active devices
 - a) Ion trap requirements
 - b) Optical modulators
- IV. Observations
 - a) Challenges
 - b) Opportunities

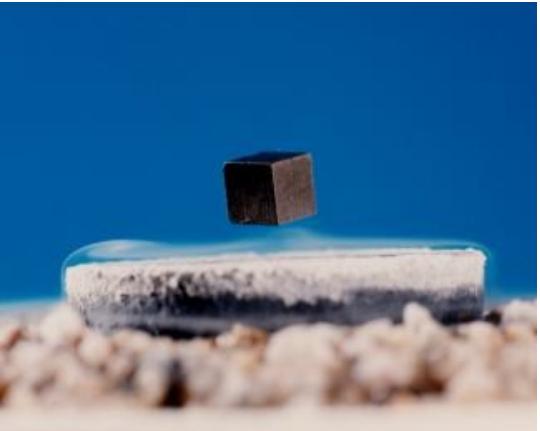
Quantum Computing



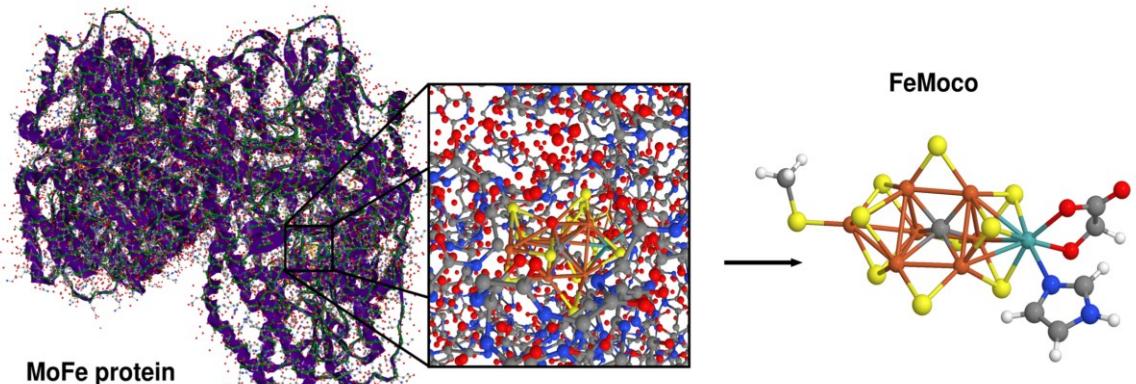
... to simulate nature at the quantum level



Richard Feynman 1982

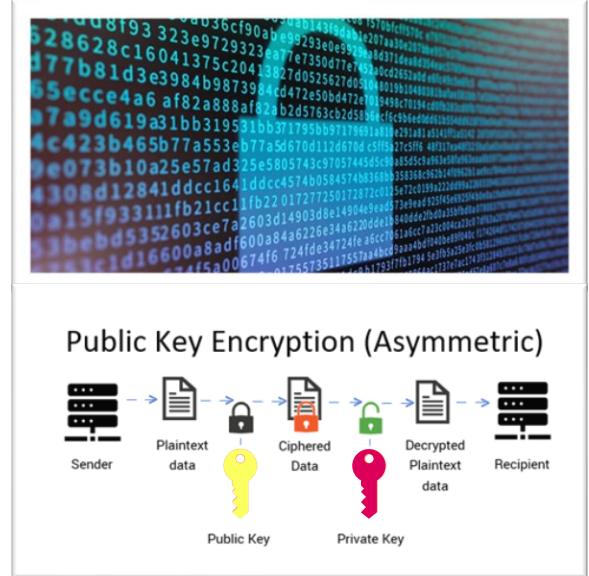


Superconductivity

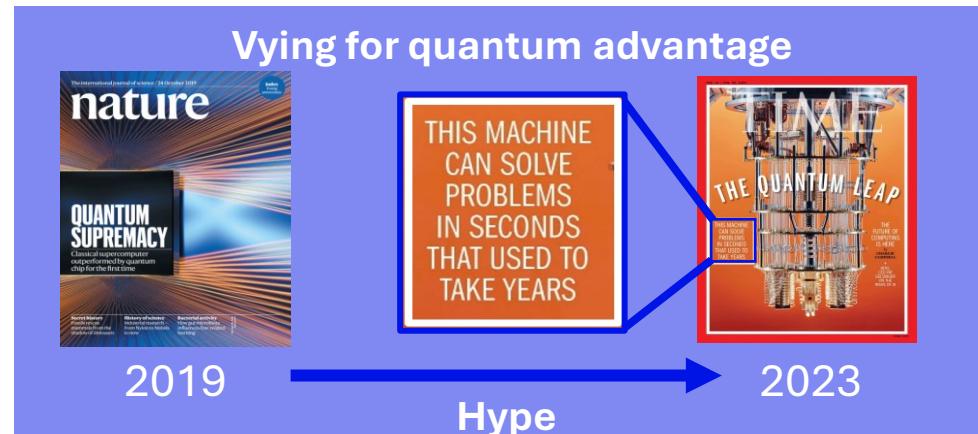


Nitrogen fixation

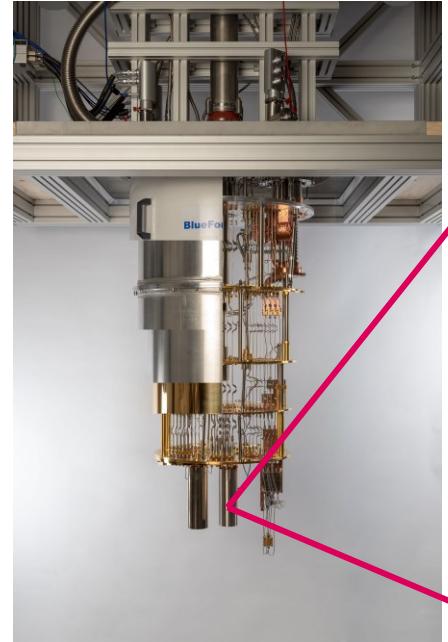
... as a general purpose computing "tool"



Prime factoring of large integers



How to build a quantum computer for qu(antum) bits.

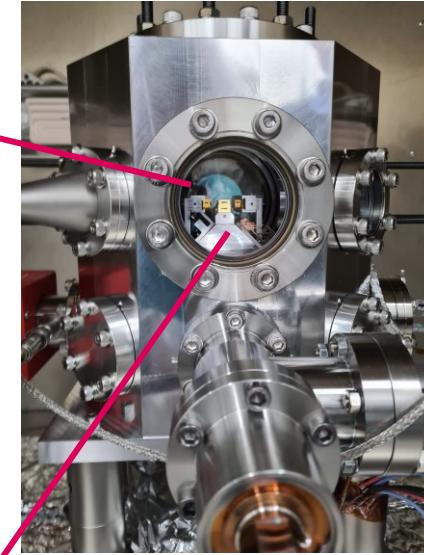
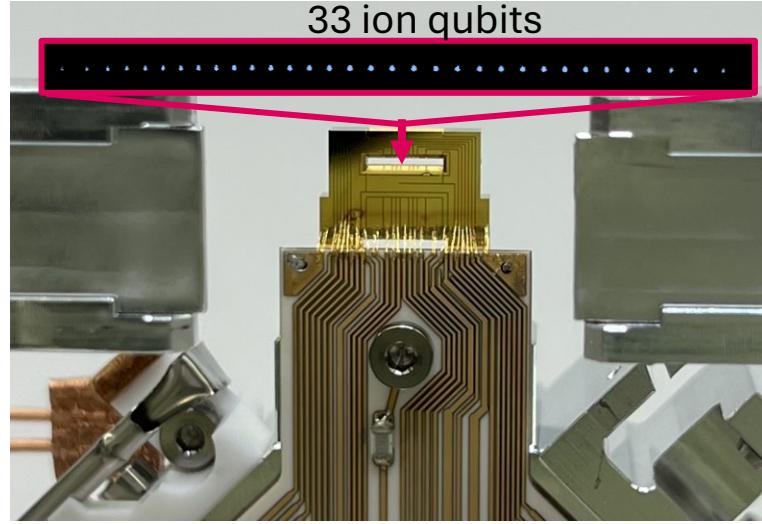


Made by humans



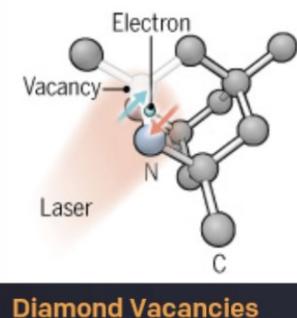
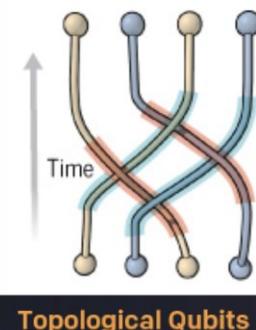
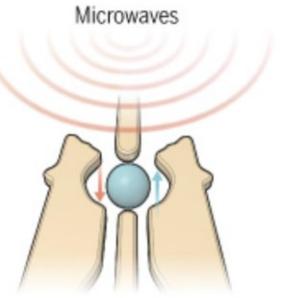
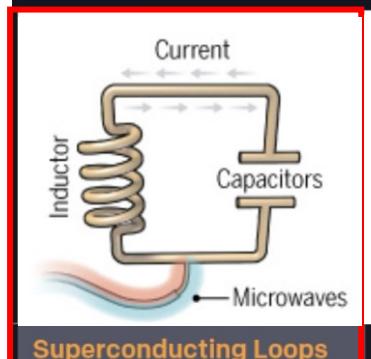
Superconducting circuits

Taken from nature

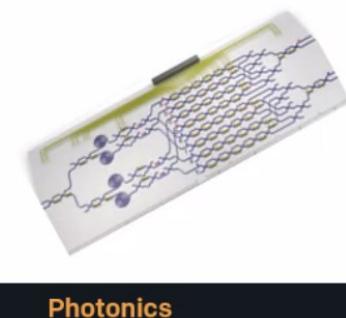
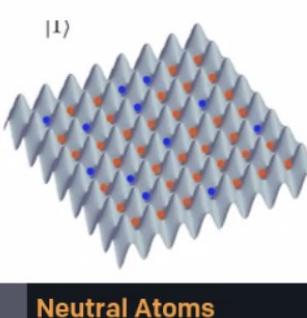
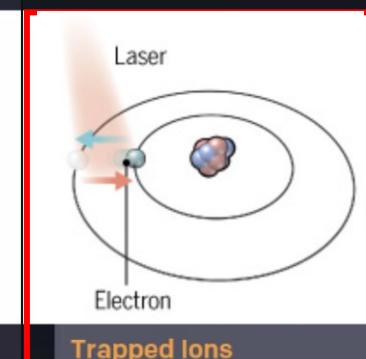


Trapped ions

Synthetic Qubits

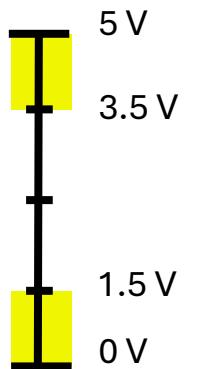


Natural Qubits



Error corrected quantum computers

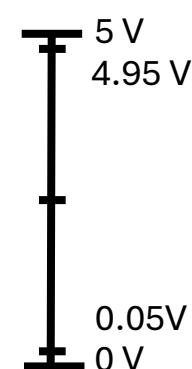
Boolean logic...



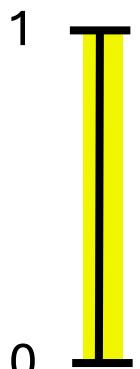
1

0

Acceptable CMOS gate input



Quantum information...

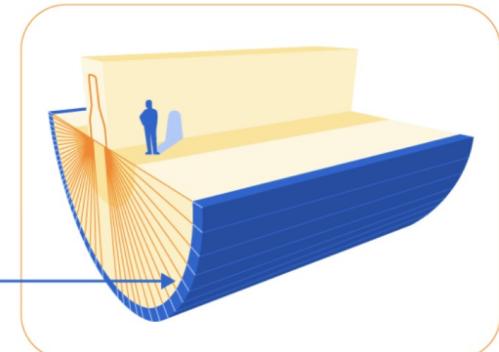
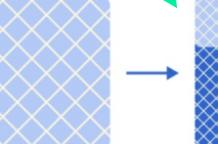
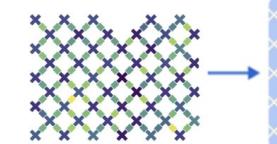


Acceptable CMOS gate output

2025 estimate
PQ:LQ → 3720 : 1
 (currently @105 qubits)



Google
Quantum AI



54

Beyond
classical
✓

M1 (2019)

10^2

Logical qubit
prototype
✓

M2 (2023)

10^3

1 long-lived
logical qubit

M3 (2025+)

10^4

Tileable module
(logical gate)

M4

10^5

Engineering
scale up

M5

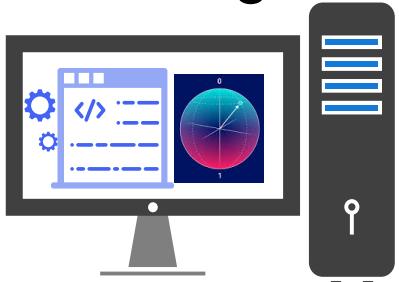
10^6

Error-corrected
quantum computer

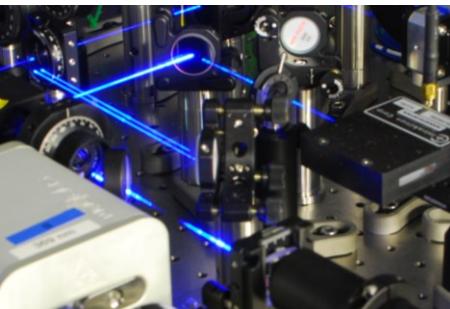
M6

Trapped ion quantum computer

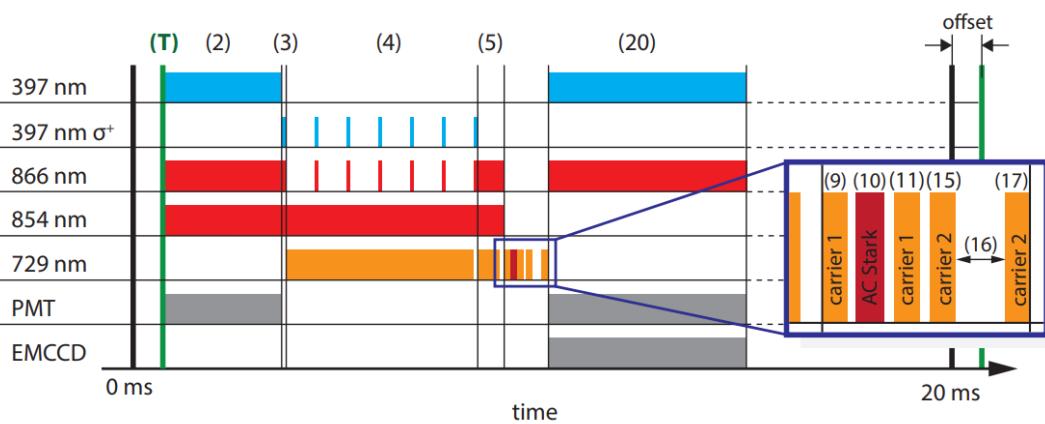
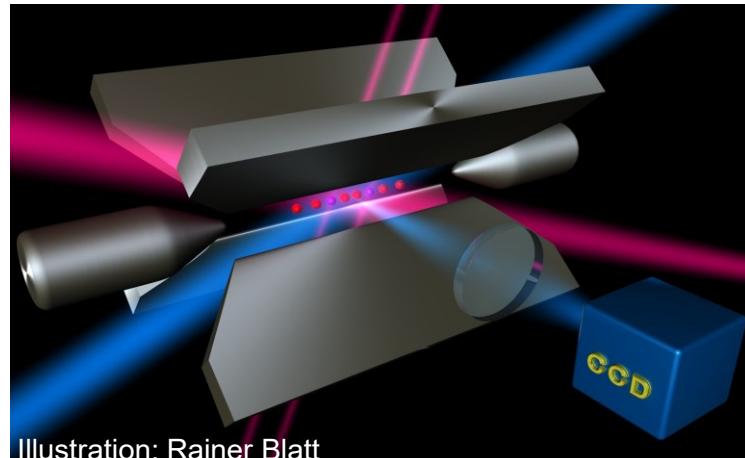
Encoding



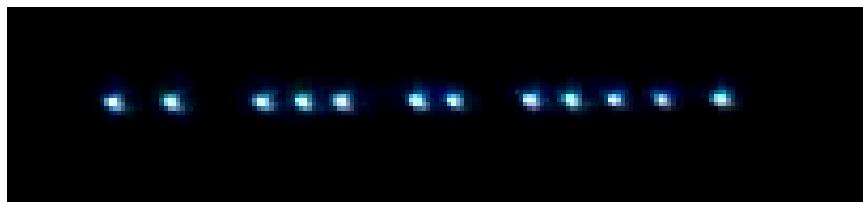
+ Laser(s)



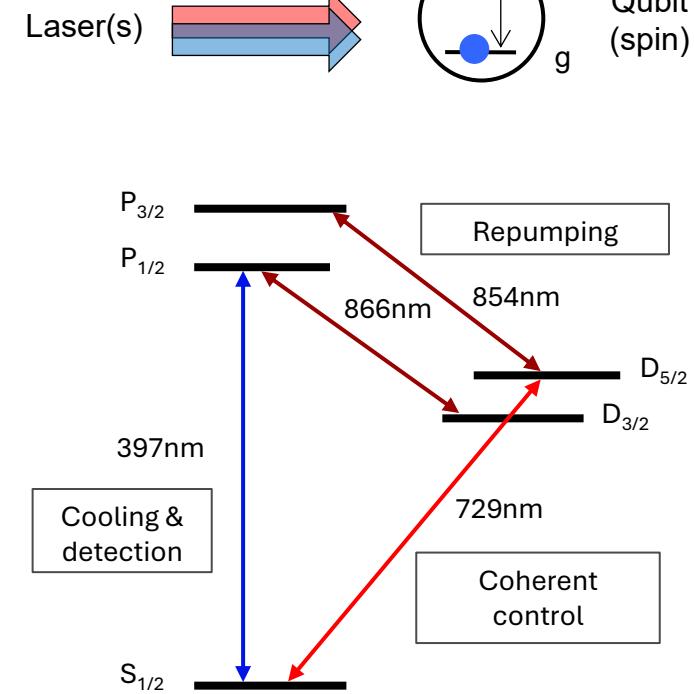
+ Ion trap



Result



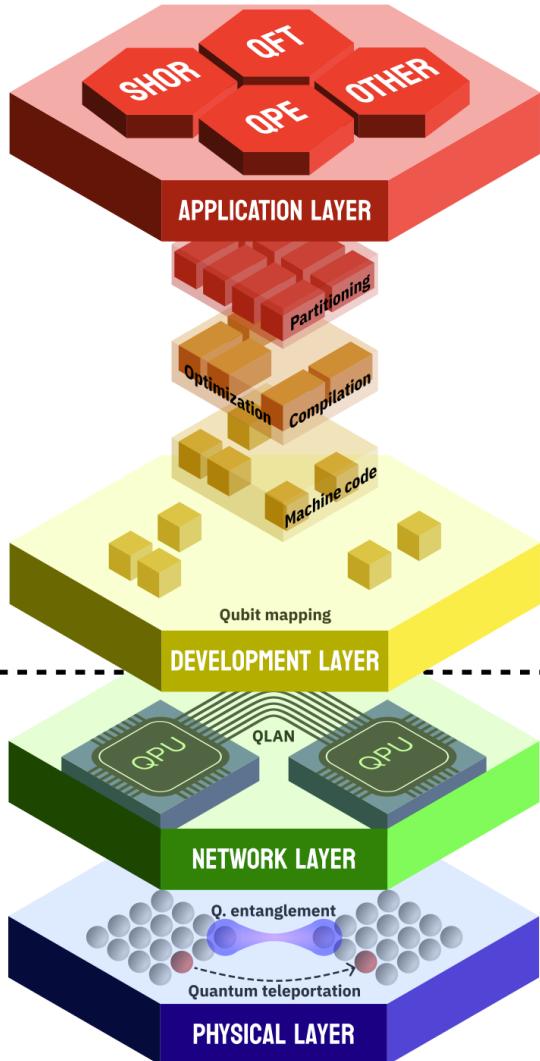
Camera



Integrated photonics

Motivation: key element for scaling quantum computers

SOFTWARE LAYERS

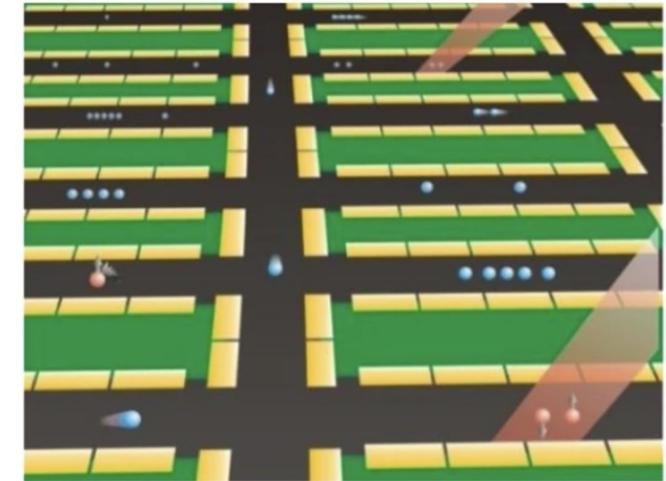


Barral et al. arXiv:2404.01265

The cabling for 56 superconducting qubits

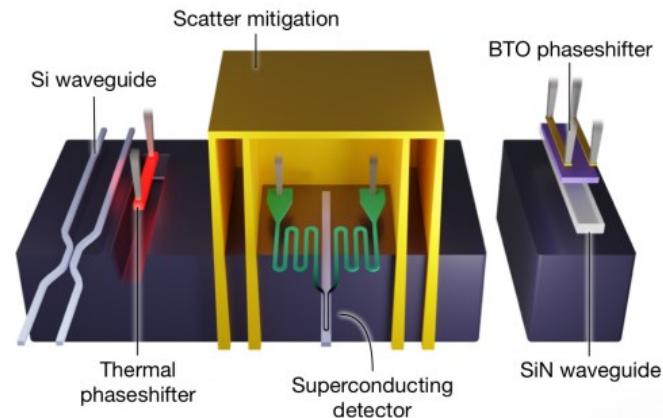


1000s of laser beams in an ion trap



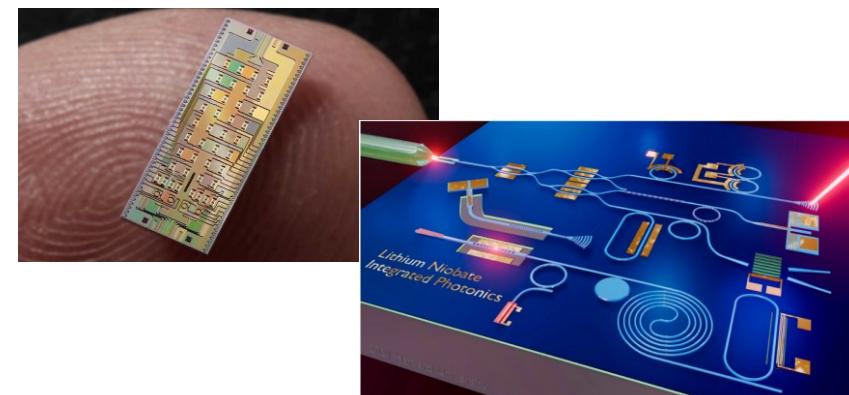
Kielpinski et al, Nature 417 709 (2002).

Quantum photonic stack



PsiQuantum Team, <https://arxiv.org/pdf/2404.17570>

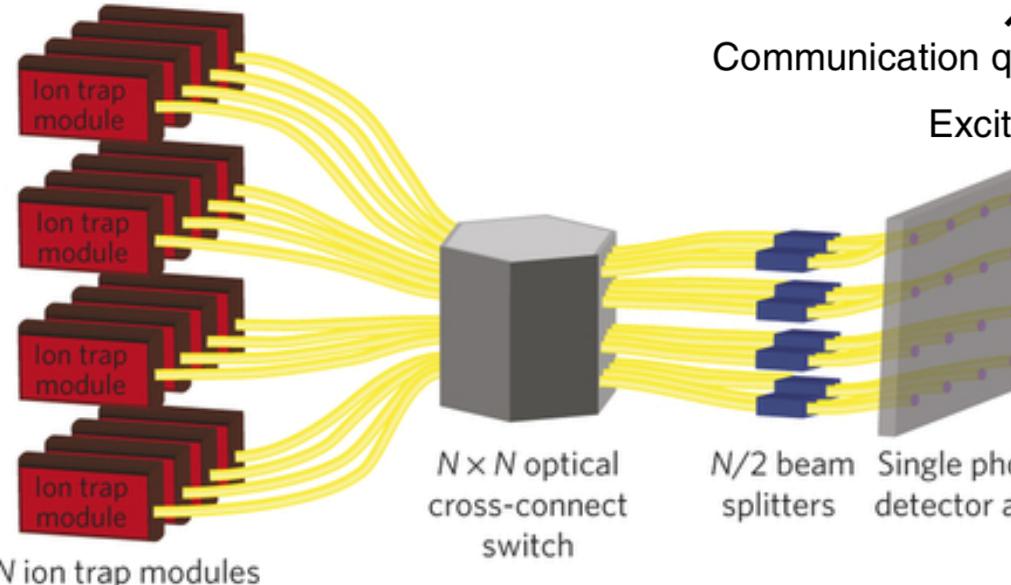
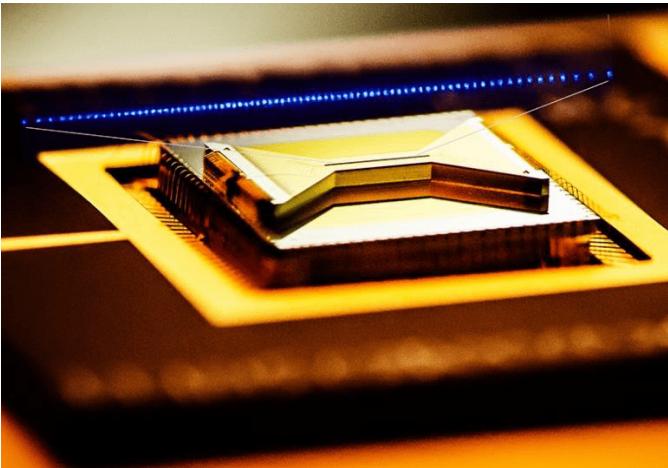
Miniaturized opto-electrical circuits



Two ways to scale up ions

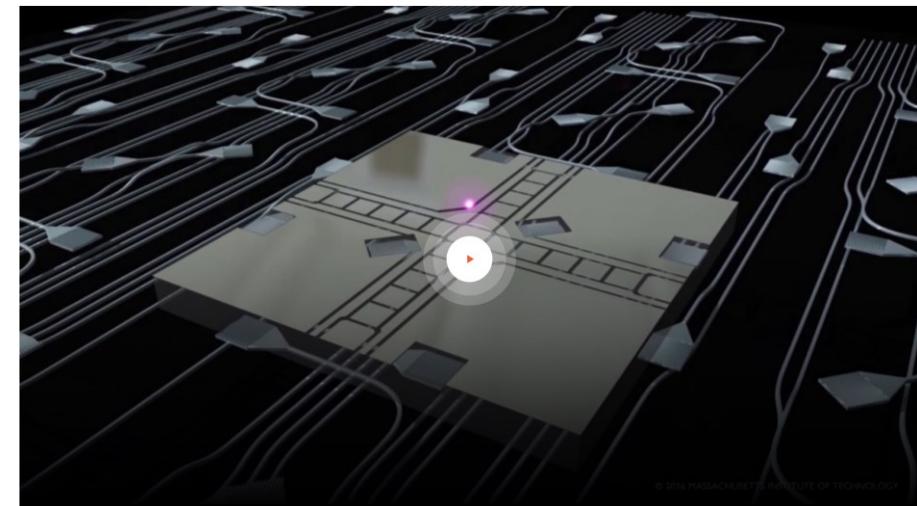
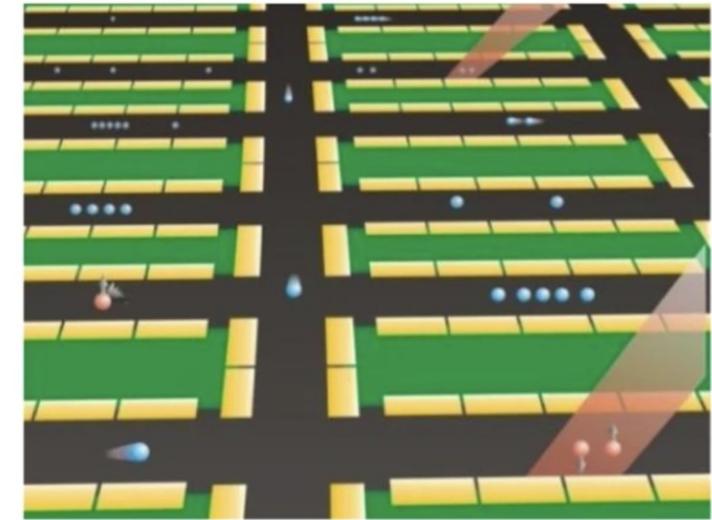
Photonic interconnects

Monroe et al. *Phys. Rev. A* **89**, 022317 (2014).



Q(uantum)CCD

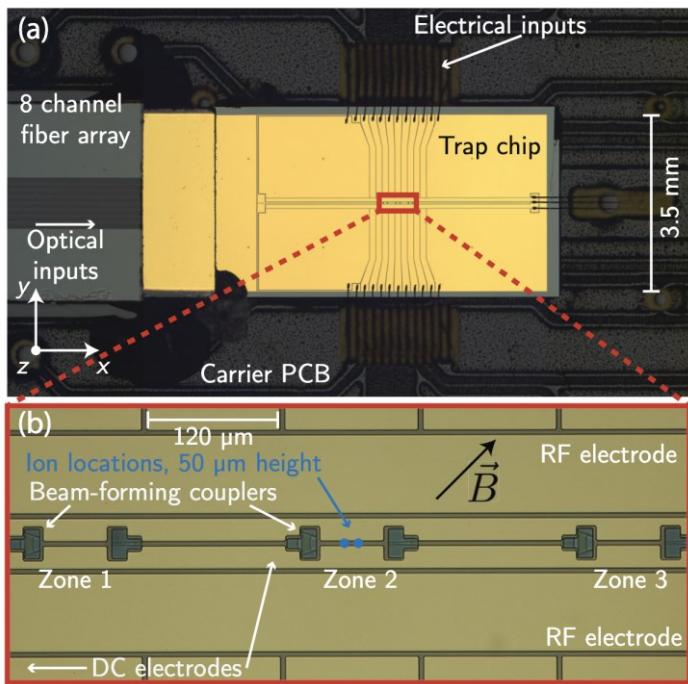
Kielpinski et al, *Nature* **417** 703 (2002).



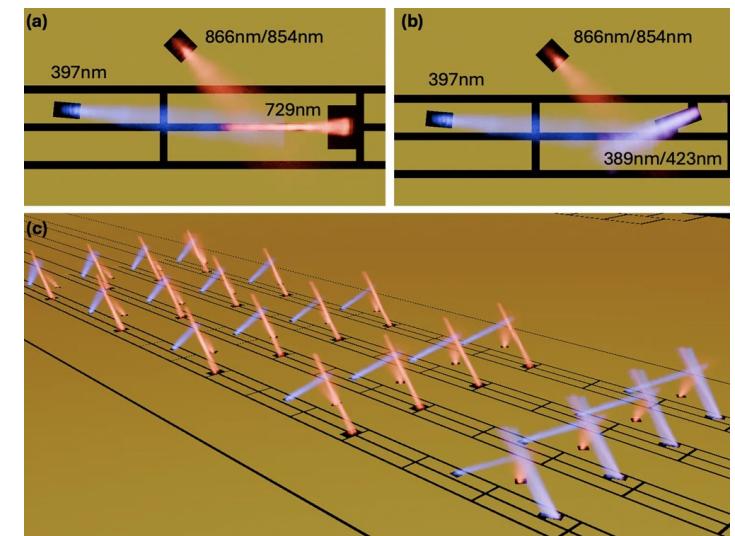
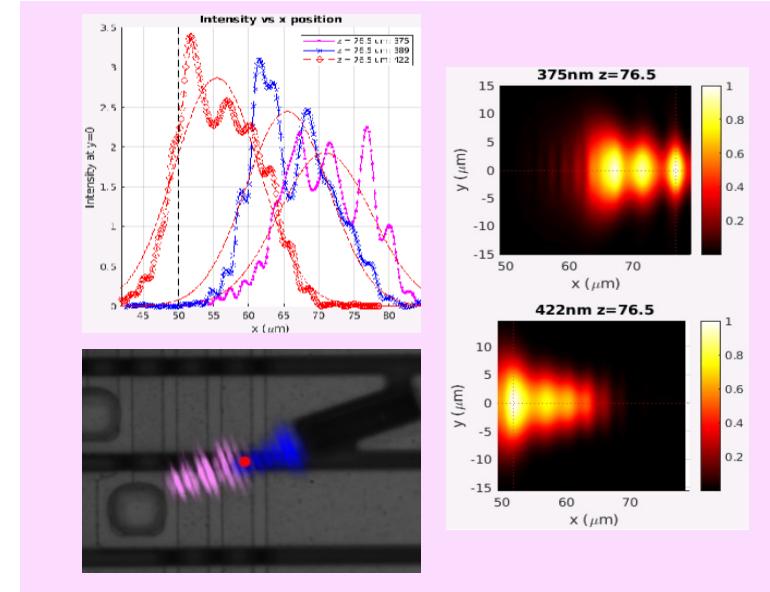
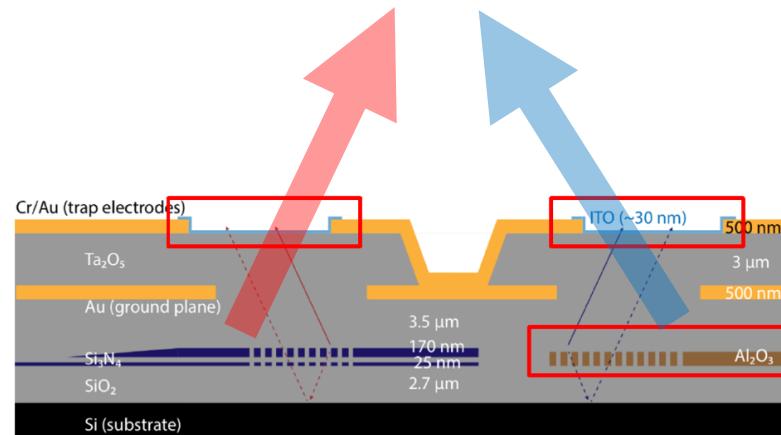
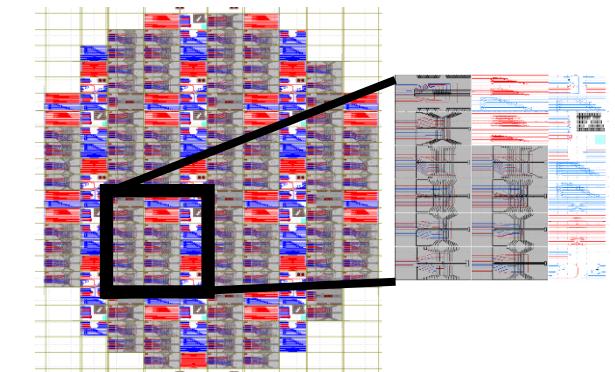
MIT Lincoln Lab (2016)

<https://www.youtube.com/watch?v=UT3ev9OgkmY>

Status

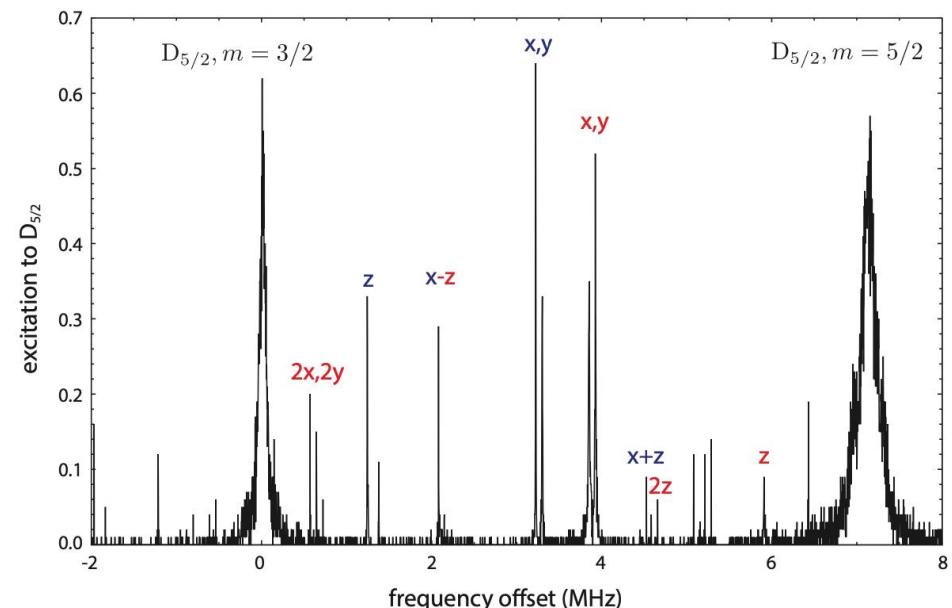
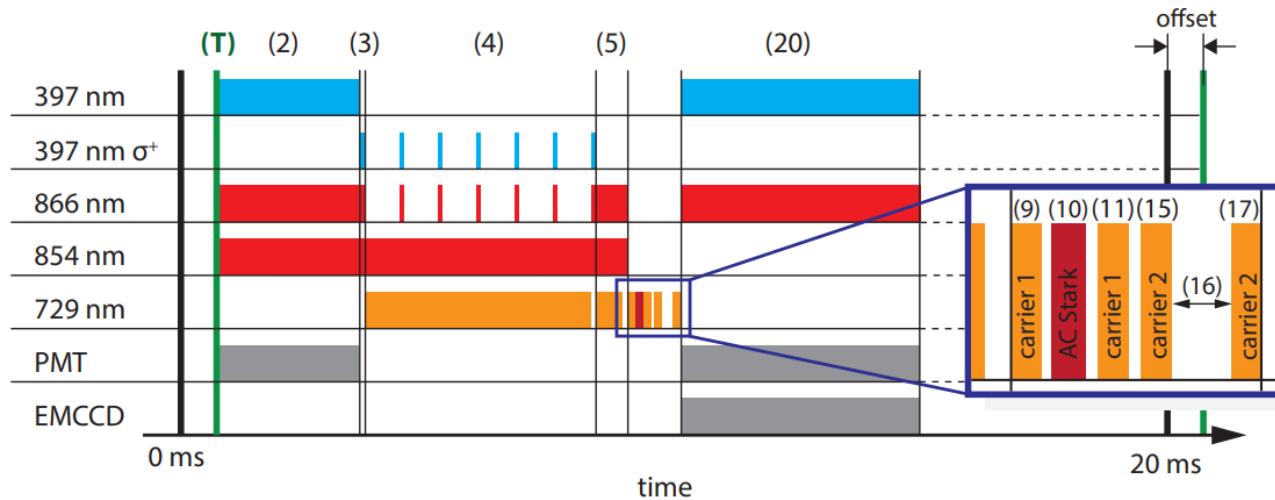


K. K. Mehta, C. Zhang, M. Malinowski, T.-L. Nguyen, M. Stadler, and J. P. Home, Nature **586**, 533-537 (2020).



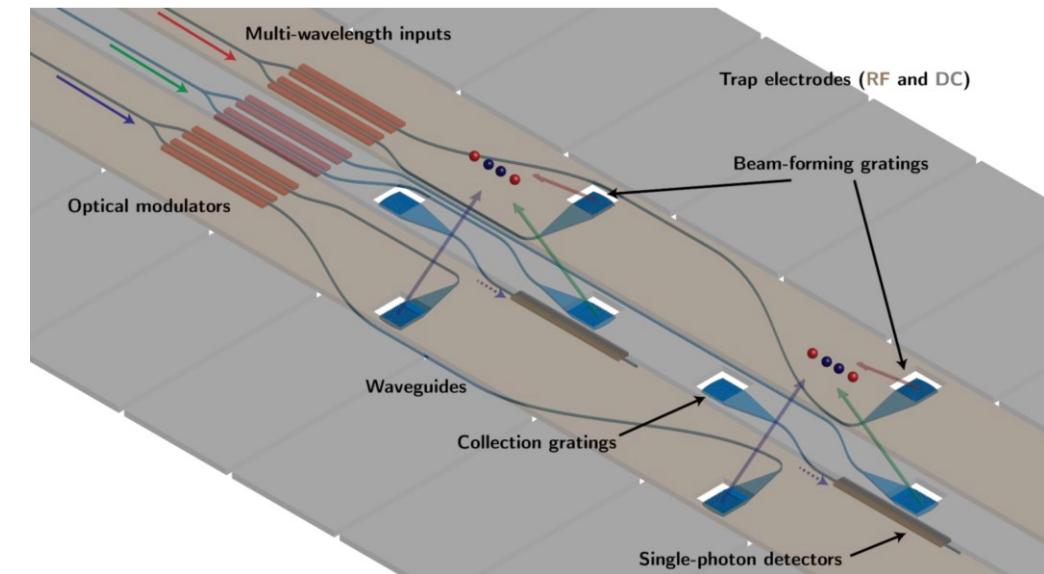
Active devices

Ion trap specifications



Ca^+

- Wavelength: 375 – 866 nm
- Power: 10 μW – 10 mW
- Target ER: >> 60 dB
- Bandwidth: 10 - 100 MHz*

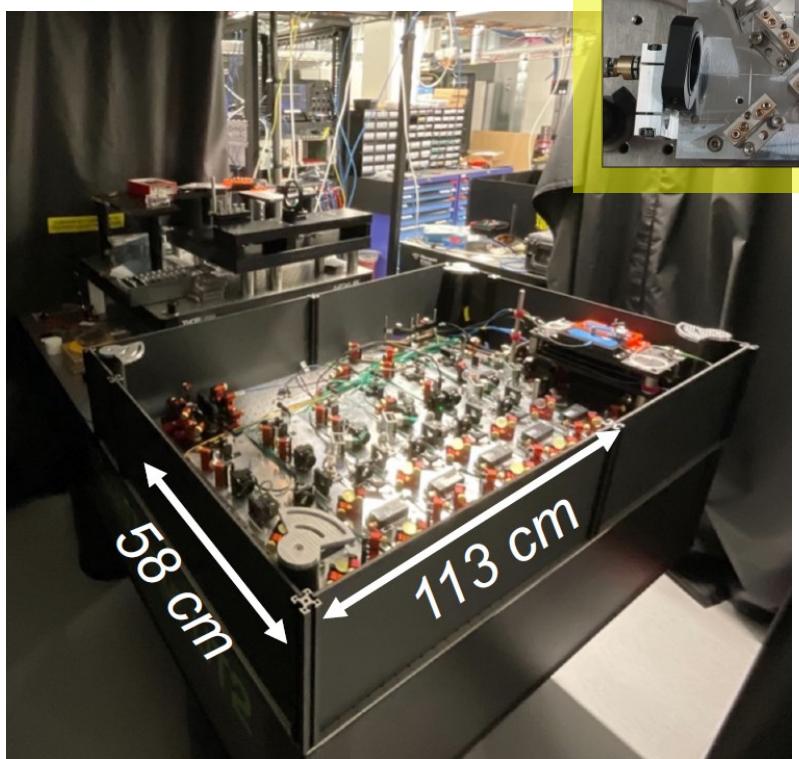


John Chiaverini and Karan K. Mehta in:
 Moody, G. et al. 2022 Roadmap on integrated quantum
 photonics. *J Phys Photonics* 4, 012501 (2022).

Active components

Signal control: Intensity, frequency and phase tuning.

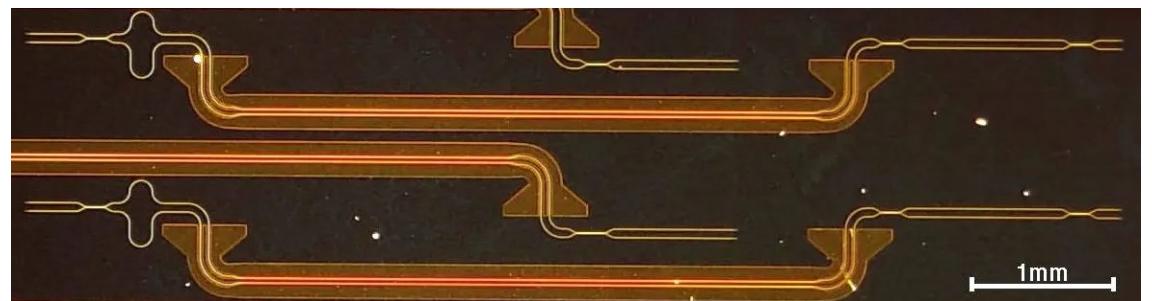
- EOM
- AOM



- TFLN iEOM



CSEM



Optical Nanomaterials Group @ ETH Zürich (G. Finco, F. Kaufmann, J. Kellner, R. Grange)

Integrated optical modulators



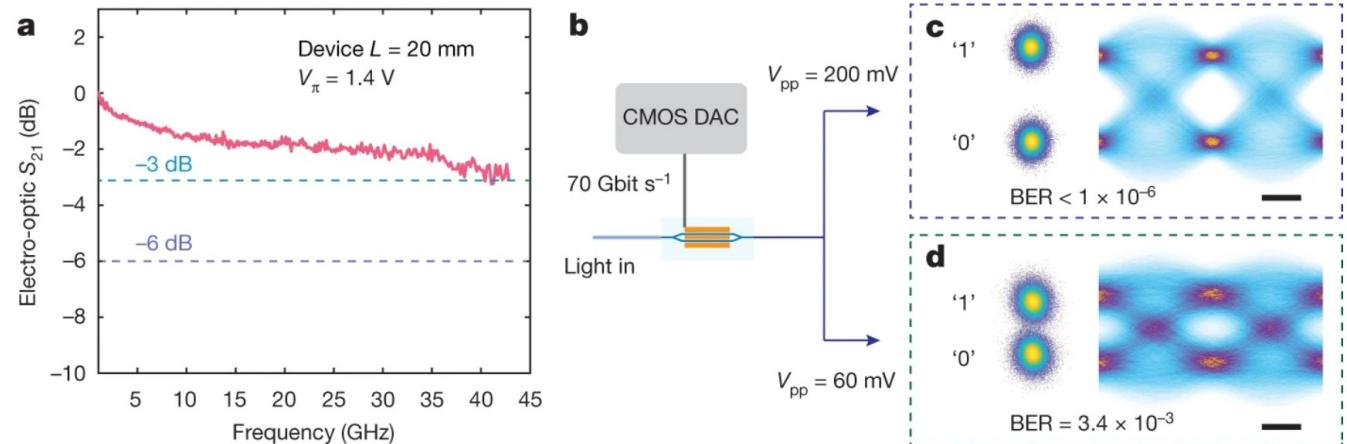
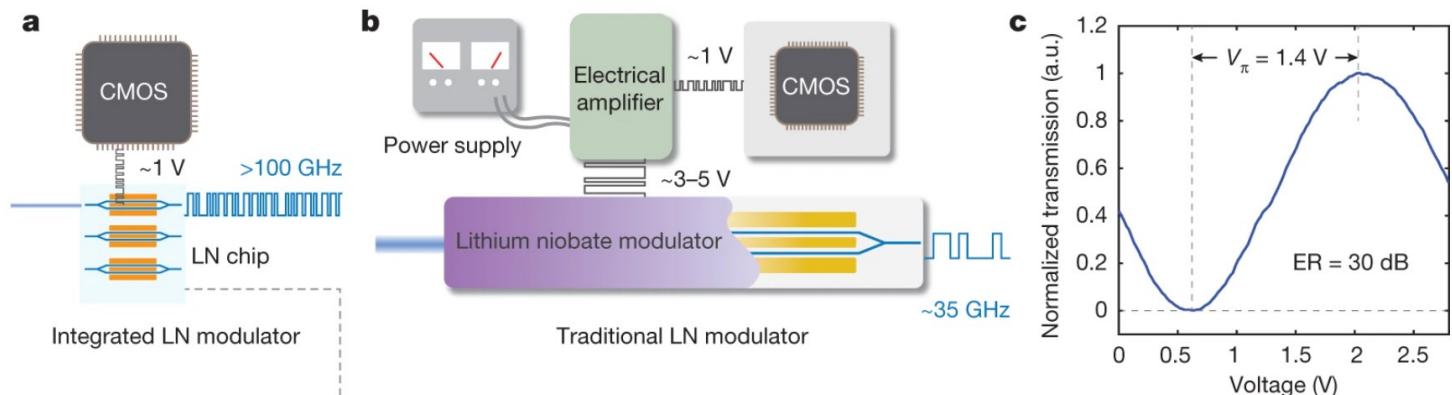
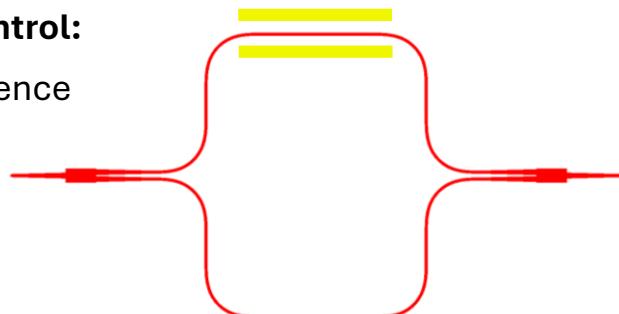
Optical phase shift:

$$\Delta\Phi = (2\pi/\lambda_0)\Delta n_{\text{eff}}x$$

Electro-optic effect, i.e. refractive index changes due to material polarization under electric field

Intensity control:

Interference

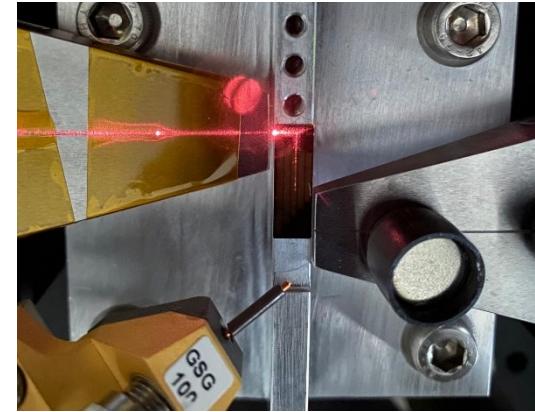


Wang, C. et al. Integrated lithium niobate electro-optic modulators operating at CMOS-compatible voltages. *Nature* **562**, 101–104 (2018). <https://doi.org/10.1038/s41586-018-0551-y>

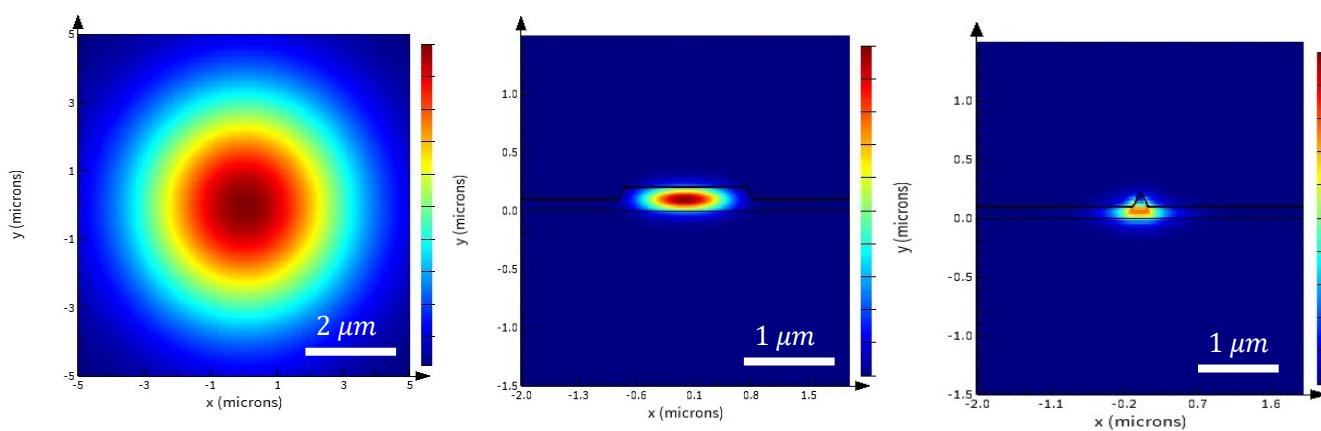
08.07.2025

Observations

Challenge 1: Low loss interfacing

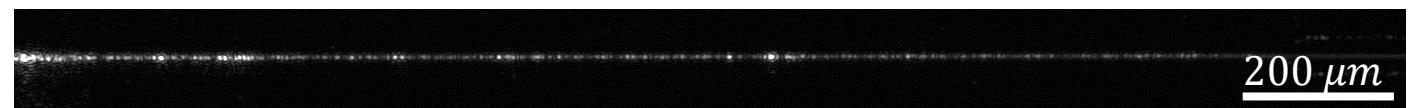


Issue: High index contrast and confined modes in thin film waveguides makes edge coupling not straight forward.

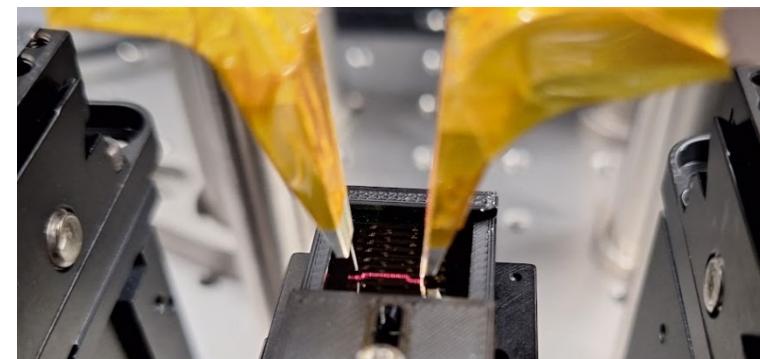
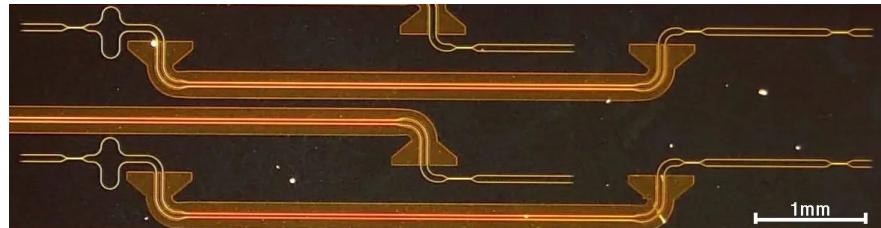


Scattering loss scales with the wavelength.

$$\alpha_{scattering} \propto \lambda^{-3}$$



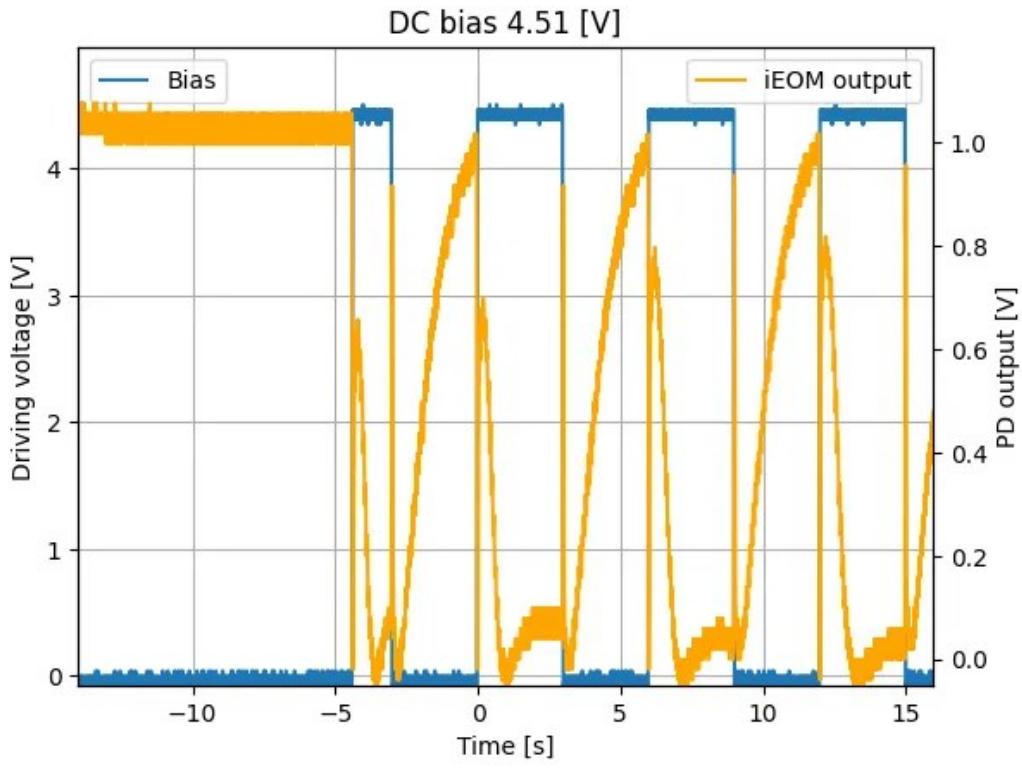
Challenge 2: Stability



$$P_{in} = 12 \text{ mW}$$

$$V_\pi = 3.5 \text{ V}^*$$

Issue: Observation of zero point drifts at lower frequencies (< 10 MHz).



- Trapped charges accumulating that may depend on fabrication, electrode distribution and temperature.

J. Holzgrafe et al, "Relaxation of the electro-optic response in thin-film lithium niobate modulators," Opt. Express 32, 3619-3631 (2024)



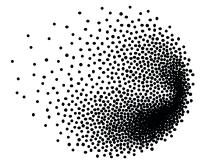
Opportunities

- Integration with other platforms to exploit benefits from each (heterogenous integration), i.e. SiN for tapers/low loss routing.
- Test other active materials including lithium tantalate.
- Implement design mitigation strategies.
- Targeted development for visible/quantum technologies.
- Leverage expertise in packaging and fabrication in the Swiss ecosystem.



Conclusions

- Passive photonic integration has been demonstrated in the ion trap field and will enable scalable laser delivery in ion traps.
- Integrated active devices have not been yet explored to the same extend and will become very relevant for large scale implementations.
- Main challenges for the use of existing technologies include interfacing issues related to the working wavelengths and lack of stability at the required time scales.



PSI

**Thank you for your
attention**

